

Typical Features

- Wide input voltage range 60-600VDC
- No load Power Consumption ≤0.6W
- Efficiency 84% (typ.)
- Over-voltage, over-current, short circuit protections
- Isolation voltage:4000Vac
- Compliant with IEC/EN61000
- Conform to RoHS regulation
- Plastic case enclosed, flame class UL94- V0
- PCB mounting



Application Field

PDD60-300S12G2N6 is a compact size, high efficiency DC/DC Converter. It has the advantages of wide input voltage range, low ripple, low temperature rise, low standby power consumption, high efficiency, high reliability, safety isolated and good EMC performance. It can be widely used in Solar power generation and home appliance energy storage and other fields. Additional circuit for EMC is recommended in this data sheet for the application with higher EMC requirement.

Typical Pr	Typical Product List									
Certificate		Output Specification			Max.	Ripple &	Efficiency			
	Part No	Power	Voltage	Current	Capacitive Load	Noise 20MHz (MAX)	@full load 300∨DC(Typ.)			
		(W)	Vo(V)	lo(mA)	uF	m∨p-p	%			
-	PDD60-300S12G2N6	60	12	5000	5400	200	84			

Note 1: The typical value of efficiency is based on the product tested after half an hour burn-in at full load.

Note 2: The full load efficiency should be in ±2% of the typical value in this table. The efficiency is calculated by the way that the full output power is divided by the input power.

Note 3: The ripple and noise are tested by the twisted pair method according to the Ripple & Noise Test Instructions in the manual.

Input Specifications									
Items	Operating Conditions	Min.	Тур.	Max.	Unit				
Input ∀oltage Range	DC Input	60	300	600	VDC				
Innut Current	60VDC@70% load	-	-	1.0	A				
Input Current	600VDC@100% load	-	-	0.3					
Surge Current	600VDC	-	80	-					
No Load Dower Consumption	Input 60∨DC	-	-	0.6					
No Load Power Consumption	Input 600∨DC	-	-	0.6	W				
External fuse recommended	-	15A/1000VDC Time-delay fuse, necessary			sary				
Hot plug	-	N/A							
Remote control	-	N/A							

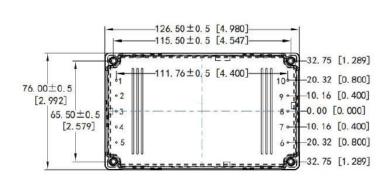


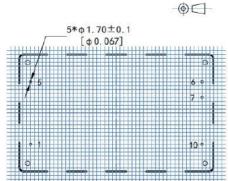
Wnit % % mS mS mS mS					
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%					
Hiccup					
%/°C					
Hiccup					
VDC					
Unit					
KHz					
**					
°C					
260±5°C, time 5-10S					
s					
%RH					
VAC					
2					
10-55Hz,10G,30 Min, along X,Y,Z					
CLASS II					
>100,000H					
Plastic in Black, flame class UL94∀-0					
126.5X76.0X40.5 mm 550g (TYP)					
32					

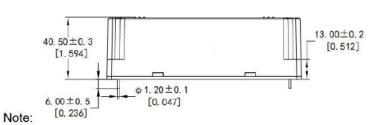


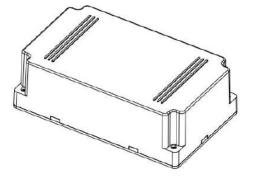
EMC Performances								
Total-item		Sub-item	Standard	Class				
		CE	CISPR22/EN55032	Class A				
	EMI	CE	Class B (with the Recommended circuit 2)					
	EIVII	RE	CISPR22/EN55032	Class A				
		RE .	CISPR22/EN99032	Class B (with the Recommended circuit 2)				
		RS IEC/EN61000-4-3		10V/m perf.Criteria A (with the Recommended circuit 2)				
EMC		cs	IEC/EN61000-4-6	10Vr.m.s perf.Criteria A (with the Recommended circuit 2)				
	EMS	ESD	IEC/EN61000-4-2	Contact ±6KV/ Air ±8KV Perf.Criteria B (with the Recommended circuit 2)				
		Surge	IEC/EN61000-4-5	Line to line ±2KV Perf.Criteria B (with the Recommended circuit 2)				
		EFT	IEC/EN61000-4-4	±4KV Perf.Criteria B (with the Recommended circuit 2)				
		PFMF	IEC/EN61000-4-8	10A/m Perf.Criteria A (with the Recommended circuit 2)				

Packaging Dimensions









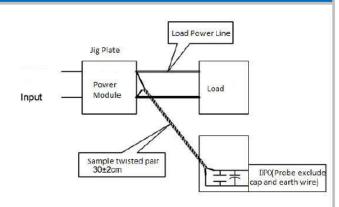
Unit: mm[inch]

General tolerance: ±0.5[±0.020]

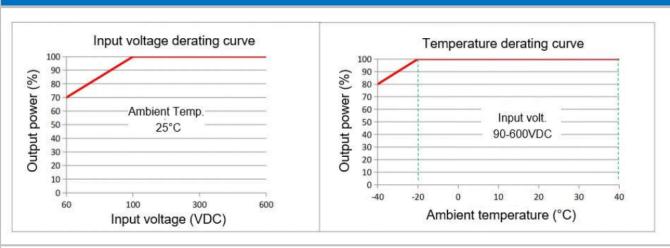
Packagir	ng Code		LxWxH							
G			126.5X76.0X40.5 mm 4.980X2.992X1.594 inch							
Pin Definitio	n									
Pin No.	1	2	3	4	5	6	7	8	9	10
Single(S)	+Vin	NP	NP	NP	-Vin	+Vo	-Vo	NP	NP	NC
Function	Input V+	No pin	No pin	No pin	Input V-	Output ∨+	Output V-	No pin	No pin	No connection

Ripple & Noise Test Instruction (Twisted Pair Method, 20MHz bandwidth)

- 1) Ripple noise test need 12# twisted pair cables, an oscilloscope which bandwidth should be set to 20MHz, 0.1uF polypropylene capacitor and 10uF high-frequency low-resistance electrolytic capacitor are connected in parallel with the probes (100M bandwidth). The oscilloscope should be set on the Sample Mode.
- 2) The output ripple noise test diagram is shown on the right. The converter output connects to the electronic load by the jig with cables which size should be defined according to the output current value. The twisted pair (length 30cm±2 cm) should be connected in parallel with the load, the location is as close as possible to the output pins or terminals. The test can be started after input power on.



Product performance curve

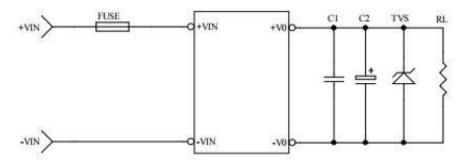


Note 1: The output power should be derated based on the input voltage derating curve at 60~100VDC.

Note 2: This product should operate at a natural air condition, please contact us if it need be used at a closed space.

Recommended Circuits for Application

1.Typical Application Circuit

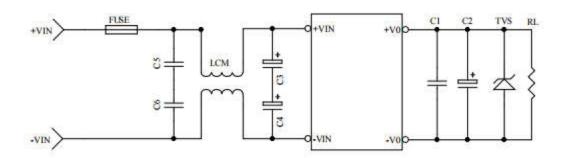


Circuit 1

Part No.	FUSE	C1	C2	TVS
PDD60-300S12G2N6	15A/1000VDC, Time-delay fuse, necessary	1206/1uF/50V	220uF/25V	SMBJ15A



2. Recommended EMC circuits



Circuit 2

Part No.	FUSE	C5, C6	C3, C4	LCM	C1	C2	TVS
PDD60-300S12G2N6	15A/1000VDC,	X2/104K/	10uF/	TD1212-15mH/	1206/1uF/50V	220uF/25V	SMBJ15A
	time-delay	275VAC	450∨DC	1-1.5A(Flat wire			
				choke)			

Application Notice:

- 1. The products should be used according to the specifications in this manual, otherwise it could be permanently damaged.
- 2.A fuse should be used at input.
- 3. The product performances in this manual cannot be guaranteed if it works at a lower load than the minimum load defined.
- 4. The product performances in this manual cannot be guaranteed if it works at over-load condition.
- 5.Unless otherwise specified, all values or indicators in this manual are tested at Ta=25°C, humidity<75%RH, rated input voltage and rated load (pure resistance load).