

Typical Feature

- ◆ Fixed input voltage, isolated & regulated, output power 0.75W
- ◆ Efficiency up to 75% (Typ.)
- ◆ Mini SMD package, international standard pin-out
- ◆ Isolation Voltage 3000VDC
- ◆ Operating Temperature from -40°C to +85°C
- ◆ Plastic case, flame class UL94 V-0



Test conditions: Unless otherwise specified, all parameter values had been tested at rated input voltage, pure resistive rated load, and at room temperature 25 °C.

Application Filed

This series of converters can be widely used in the fields of instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List											
Certificate	Part No.		Voltage e (VDC)		utput e/Current	(mA @R	Current)Typ. ated age	Max. Capacit ive Load	Ripple & Noise 20MHz (mVp-p)	Effici (%) (load/i	@full rated
		Rated	Range	Vo (VDC)	lo (mA) Max / Min	Full load	No Load	uF (Max)	Max/Typ	Min	Тур
-	PWV75-3V3S3V3A3NT	3.3	3.135 - 3.465	3.3	200/20	290	8	2400	80/50	67	70
-	PWV75-05S3V3A3NT		4.75	3.3	200/20	200	6	2400	80/50	67	70
-	PWV75-05S05A3NT	5	-	5	150/15	205	6	2400	80/50	70	73
-	PWV75-05S12A3NT		5.25	12	62/7	186	8	560	80/50	72	75
-	PWV75-12S3V3A3NT		11.4	3.3	200/20	86	8	2400	80/50	67	70
-	PWV75-12S05A3NT	12	-	5	150/15	83	8	2400	80/50	70	73
-	PWV75-12S12A3NT		12.6	12	62/7	83	8	560	80/50	72	75
-	PWV75-24S3V3A3NT		22.8	3.3	200/20	45	8	2400	80/50	67	70
-	PWV75-24S05A3NT	24	-	5	150/15	41	8	2400	80/50	70	73
-	PWV75-24S12A3NT		25.2	12	62/7	41	8	560	80/50	72	75

Note - The ripple and noise are tested by the twisted pair method.

Input Specifications								
Item	Operating Condition	Min.	Тур.	Max.	Unit			
	3.3∨dc Input	-0.7		7				
Innut innuch welfers (4 Cooper d May)	5∨dc Input	-0.7		9	-			
Input inrush voltage (1Second Max.)	9Vdc Input	-0.7	-	12	Vdc			
	12∨dc Input	-0.7		18				



	15∨dc Input	-0.7		21	
	24Vdc Input -0.7 30				
Input Filter Type	Capaci	itor Filter			
Hot Plug	Unavailable				

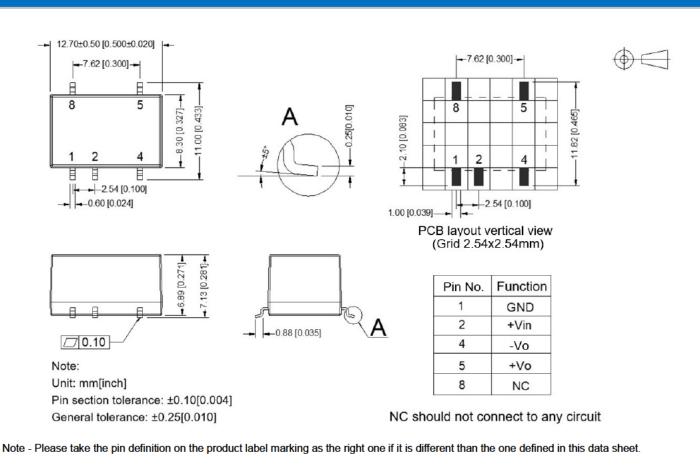
Output Specifications								
Item	Operating Condition	Min.	Typ.	Max.	Unit			
Output Power		0.07		0.75	W			
Output Voltage Accuracy	Rated input voltage, full load		±2	±3				
Load Regulation	10%-100% load			±3	%			
Line Regulation	Input voltage change ±1%			±0.25				
Temperature Drift Coefficient	Full load			±0.03	%/°C			
Short Circuit Protection	Continuous, Self-recovery							

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Item	Operating Condition	Min.	Тур.	Max.	Unit	
Switching Frequency	Rated input voltage, full load 260				KHz	
Operating Temperature	Refer to the temperature derating curve	-40		+85		
Storage Temperature		-55	-	+125	•	
Case Temperature Rise	Operating at Ta =25°C		30°		. ℃	
Pin Soldering Temperature	1.5mm from the case, 10S			300		
Reflow Temperature	Peak temperature Tc≤250℃, the max	imum time a	above 217°C	is 60S		
Relative Humidity	No condensing	5	-	95	%RH	
Isolation Voltage	lation Voltage Input-Output, test 1min, leakage current <1mA				VDC	
Insulation Resistance	Input-Output, @ 500√dc			ΜΩ		
Isolation Capacitor	Input/Output,100KHz/0.1V		20		pF	
MTBF MIL-HDBK-217F@25°C		3500			K hours	
Vibration	10-150Hz,10G,30Min, along X,Y					
Case Material	Plastic in Black, flame class UL94 ∨-0					
Product Weight	1.4 g (Typ.)					
Cooling Method	Natural air					

EMC Performance								
EMI	CE	CISPR32/EN55032 CLASS B (with Recommended EMC Circuit)						
EIVII	RE	CISPR32/EN55032 CLASS B (with Recommended EMC Circuit)						
EMS	ESD	IEC/EN61000-4-2 Air ±8kV, Contact ±4kV perf. Criteria B						



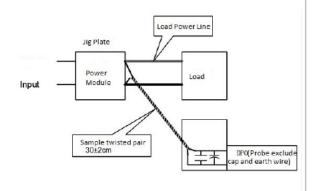




Packaging Code	Dimensions L x W x H			
A3NT	12.70x11.00x7.13 mm	0.500x0.433x0.281 inch		

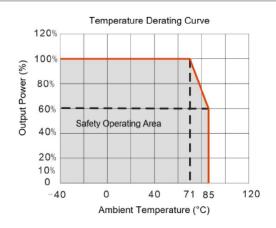
Ripple & Noise Test Instructions (Twisted Pair Method, 20MHz Bandwidth)

- 1. The Ripple & noise test need 12# twisted pair cables, an oscilloscope which bandwidth should be set to 20MHz, 0.1uF polypropylene capacitor and 10uF high-frequency low-resistance electrolytic capacitor are connected in parallel with the probes (100M bandwidth). The oscilloscope should be set at the Sample Mode.
- 2. The test diagram is shown on the right. The converter output connects to the electronic load by the jig with cables which size should be defined according to the output current value. The twisted pair (length 30cm±2 cm) should be connected in parallel with the load, the location is as close as possible to the output pins or terminals. The test can be started after input power on.









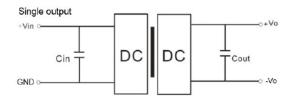
Recommended Circuits for Application

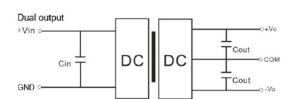
① Output load requirements

The maximum capacitive load of the product was tested at the Rated full load. The converter may not start or be damaged if the output capacitor exceeds this value.

2 Recommended circuits for application

To effectively decrease the input and output ripple and noise, a capacitor filter should be connected at the input and output, the application circuit is shown in the figure below. The suitable filter capacitors should be chosen as the recommended capacitive load values in Table 1. The converter could not start if the capacitance is too big.

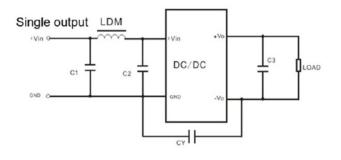




Recommended Capacitive Load Value Table (Table 1)

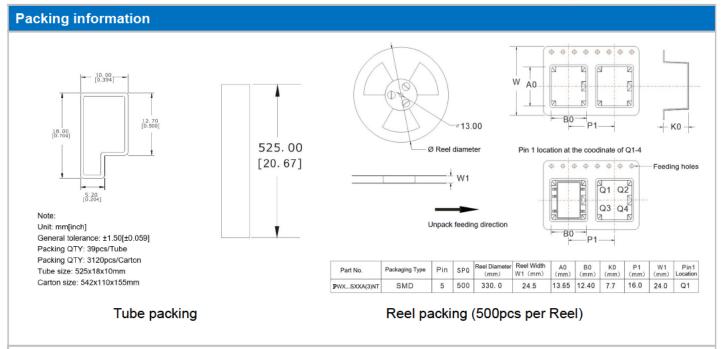
Vin (Vdc)	Cin	Single Vout (Vdc)	Cout (µF)	Dual Vout (Vdc)	Cout (µF)
5	10 µ F/16V	3. 3	10 µ F/16V	± 3.3	4.7 µF/16V
12	2. 2 µ F/25V	5	10 µ F/16V	±5	4.7 µ F/16V
15	2. 2 µ F/25V	9	2. 2 µ F/25V	±9	2. 2 µ F/25V
24	1 µ F/50V	12	2. 2 µ F/25V	±12	1 µF/25V
		15	1 μF/25V	±15	1 µF/16V
		24	1 µF/50V	±24	0. 47 μF/50

3 Recommended EMC Circuit



Input v	oltage	5VDC	12/15/24VDC		
	C1/C2	4. 7μF/16V	4. 7μF/50V		
EMI	CY	270pF/4KV	270pF/4KV		
EMI	С3	Refer to Cou	it in Table 1		
	LDM	6. 8 µ H	6.8 µ H		





Application Notice

- 1. This product cannot be used in parallel, and it does not support hot-plugging.
- 2. The product performance in this manual cannot be guaranteed if it works at a lower load than the minimum load condition.
- 3. All values or indicators in this manual had been tested based on Aipupower test specifications.